What is claimed is:

1. A method of backing up BIOS settings stored in a CMOS memory in a computer system by a DMI memory, the method comprising steps of:

executing a power on self test (POST) procedure after powering on the computer system;

detecting BIOS settings stored in the CMOS memory; and

writing predetermined BIOS settings stored in the DMI memory into the CMOS memory if the BIOS settings stored in the CMOS memory are abnormal.

2. The method of claim 1, wherein the computer system subsequently accomplishes the POST procedure if the BIOS settings stored in the CMOS memory are normal.

3. The method of claim 1, wherein the BIOS settings are backed up in a memory block of the DMI memory.

4. The method of claim 1, wherein the DMI memory is located in a flash memory.

5. A method of backing up BIOS settings stored in a CMOS memory in a computer system by a DMI memory, the method comprising the steps of:

executing a power on self test (POST) procedure after powering on the computer system;

detecting whether the BIOS settings stored in the CMOS memory are normal;

determining whether to ignore a reloading function;

detecting header data of the DMI memory;

determine whether to access the DMI memory according to whether an enabling signal has been set; and

writing predetermined BIOS settings stored in the DMI memory into the CMOS memory.

6. The method of claim 5, wherein the computer system subsequently accomplishes the POST procedure if the BIOS settings stored in the CMOS memory are normal.

7. The method of claim 5, wherein the computer system subsequently accomplishes the POST procedure if the reloading function is ignored.

8. The method of claim 5, wherein the computer system subsequently accomplishes the POST procedure if the DMI memory has no header information.

9. The method of claim 5, wherein the computer system subsequently accomplishes the POST procedure if the enabling signal is not set.

10. The method of claim 5, wherein the BIOS settings are backed up in a memory block of a DMI memory.

11. The method of claim 5, wherein the DMI memory is located in a flash memory.

12. A method of backing up BIOS settings stored in a CMOS memory in a computer system into a DMI memory, comprising:

entering a BIOS setting menu;

querying whether BIOS settings being save after exiting the BIOS setting menu;

backing up the BIOS settings into the DMI memory.

13. The method of claim 12, wherein the BIOS settings are stored in a memory block of the DMI memory.

14. The method of claim 12, wherein the DMI memory is located in a flash memory.

15. The method of claim 12, wherein the BIOS settings are not saved if saving BIOS settings after exiting the BIOS settings menu is not required.

16. A method of backing up a BIOS setting stored in the CMOS memory of a computer system by a DMI memory, the DMI memory including a memory block for storing predetermined BIOS settings in the computer system, the method comprising the following steps:

storing the predetermined BIOS settings stored in the CMOS memory to the memory block of the DMI memory;

updating the BIOS settings stored in the CMOS memory according to the predetermined BIOS setting stored in the memory block of the DMI memory.

17. The method of claim 16, wherein the corresponding memory block of the DMI memory is located in a flash memory.

18. The method of claim 16, wherein the CMOS memory is located in a south bridge chipset.

19. A computer system for backing up BIOS settings in a memory block of a flash memory, comprising:

a central processing unit (CPU);

a front side system bus (FSB) for connecting a north bridge chip to the CPU;

a double data rate (DDR) memory bus for connecting the north bridge chip to a memory module;

an accelerated graphic port (AGP) bus for connecting the north bridge chip to a display card module;

a peripheral component interconnect (PCI) bus for connecting a south bridge chip to a plurality of peripheral devices;

a CMOS memory located in the south bridge chip; and

a flash memory located on a motherboard of the computer system, wherein the flash memory comprises a first memory block for a DMI memory.

20. The computer system of claim 19, wherein the BIOS settings are stored in the CMOS memory.

21. The computer system of claim 19, wherein the memory block of the flash memory comprises a memory block for the DMI memory, and the DMI memory backs up the BIOS settings.